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4 An SRAM-programmable field-configurable memory

Ngai, T.; Rose, J.; Wilton, S.J.E.;

Custom Integrated Circuits Conference, 1995., Proceedings of the IEEE 1995, 1-4 May 1995

Pages: 499 - 502

[Abstract] [PDF Full-Text (328 KB)]

5 `Configurable array logic technology at the chip and board level'

User-Configurable Logic - Technology and Applications, IEE Colloquium on , 1 Mar 1991

Pages:3/1 - 3/3

[Abstract] [PDF Full-Text (80 KB)] IEE CNF

6 Identification and classification of single-event upsets in the configuration memory of SRAM-based FPGAs

Ceschia, M.; Violante, M.; Reorda, M.S.; Paccagnella, A.; Bernardi, P.; Rebaudengo M.; Bortolato, D.; Bellato, M.; Zambolin, P.; Candelori, A.; Nuclear Science, IEEE Transactions on, Volume: 50, Issue: 6, Dec. 2003

Pages: 2088 - 2094

[Abstract] [PDF Full-Text (315 KB)] IEEE JNL

7 Supply and substrate noise tolerance using dynamic tracking clusters in configurable memory designs

Meng-Fan Chang; Kuei-Ann Wen; Ding-Ming Kwai; Quality Electronic Design, 2004. Proceedings. 5th International Symposium on , 2004

Pages:297 - 302

[Abstract] [PDF Full-Text (247 KB)] IEEE CNF

8 A 20ns CMOS functionable gate array with a configurable memory

Sano, T.; Matsukuma, S.; Hashimoto, K.; Ohuchi, Y.; Kudo, O.; Yamamoto, H.; Solid-State Circuits Conference. Digest of Technical Papers. 1983 IEEE International, Volume: XXVI, Feb 1983

Pages:146 - 147

[Abstract] [PDF Full-Text (560 KB)] IEEE CNF

9 Ferroelectric memory based secure dynamically programmable gate array

Masui, S.; Ninomiya, T.; Oura, M.; Yokozeki, W.; Mukaida, K.; Kawashima, S.; VLSI Circuits Digest of Technical Papers, 2002. Symposium on , 13-15 June 2002 Pages: 200 - 203

[Abstract] [PDF Full-Text (419 KB)] IEEE CNF

10 An 8 ns BiCMOS 1 Mb ECL SRAM with a configurable memory array size Tran, H.; Fung, K.; Bell, D.; Chapman, R.; Harward, M.; Suzuki, T.; Havemann, R. Eklund, R.; Fleck, R.; Le, D.; Wei, C.; Iyengar, N.; Rodder, M.; Haken, R.; Scott,

D.; Solid-State Circuits Conference, 1989. Digest of Technical Papers. 36th ISSCC., 1989 IEEE International, 15-17 Feb. 1989

Pages:36 - 37, 283

[Abstract] [PDF Full-Text (228 KB)] IEEE CNF

11 A ferroelectric memory-based secure dynamically programmable gate array

Masui, S.; Ninomiya, T.; Oura, M.; Yokozeki, W.; Mukaida, K.; Kawashima, S.; Solid-State Circuits, IEEE Journal of , Volume: 38 , Issue: 5 , May 2003 Pages:715 - 725

[Abstract] [PDF Full-Text (1441 KB)] IEEE JNL

Radiation effects on current field programmable technologies Katz, R.; LaBel, K.; Wang, J.J.; Cronquist, B.; Koga, R.; Penzin, S.; Swift, G.;

Nuclear Science, IEEE Transactions on , Volume: 44 , Issue: 6 , Dec. 1997 Pages: 1945 - 1956

[Abstract] [PDF Full-Text (1568 KB)] IEEE JNL

13 Testing for the programming circuit of LUT-based FPGAs

Michinishi, H.; Yokohira, T.; Okamoto, T.; Inoue, T.; Fujiwara, H.; Test Symposium, 1997. (ATS '97) Proceedings., Sixth Asian, 17-19 Nov. 1997 Pages: 242 - 247

[Abstract] [PDF Full-Text (528 KB)] IEEE CNF

14 Analyzing SEU effects is SRAM-based FPGAsb

Violante, M.; Ceschia, M.; Sonza Reorda, M.; Paccagnella, A.; Bernardi, P.; Rebaudengo, M.; Bortolato, D.; Bellato, M.; Zambolin, P.; Candelori, A.; On-Line Testing Symposium, 2003. IOLTS 2003. 9th IEEE, 7-9 July 2003 Pages:119 - 123

[Abstract] [PDF Full-Text (325 KB)] IEEE CNF

15 Ion beam testing of SRAM-based FPGA's

Bellato, M.; Ceschia, M.; Menichelli, M.; Papi, A.; Wyss, J.; Paccagnella, A.; Radiation and Its Effects on Components and Systems, 2001. 6th European Conference on , 10-14 Sept. 2001 Pages:474 - 480

[Abstract] [PDF Full-Text (456 KB)] IEEE CNF

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